

# Simulation and Analysis of a Multilevel Inverter for Power Quality Improvement

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**Abstract** Interconnections of grid in power system have been done for economic purpose, reduction in electricity cost and to improve the reliability. However switching periods become large and there are some discrete operations present in the power grids. It becomes difficult to handle such changing loads smoothly. Immediate response to transient oscillations and to avoid severe black-outs are the challenging part of the interconnected grid system. In the family of FACTS, STATCOM shows remarkable results in power quality improvement. In this paper different levels of MLI has been discussed and simulated through which analysis of THD has been done.

Static Synchronous Compensator (STATCOM) regulates the voltage and corrects the power factor at the Point of Common Coupling (PCC) by injecting reactive power [1]. Also, this device plays a vital role as a stability aid for small and large transient disturbances in an interconnected power system. This paper deals only with power-factor correction mode and show the Total Harmonic Distortion (THD). This paper presents analytical approach of a cascade H bridge 11 level multilevel STATCOM connected to power system for voltage regulation applications. The main objective of this paper is to maintain the voltage stability by compensating the reactive power in the power system. The multilevel STATCOM which can be used at the PCC, for improving power quality is modelled and simulated using proposed control strategy [2]. Simulations using MATLAB / SIMULINK are carried out to verify the performance of the proposed controller.

**Keywords-** Multilevel inverter (MLI), Total Harmonic Distortion (THD), Power-Factor Correction, Point of common coupling (PCC).

## I. INTRODUCTION

Any change in the rated value of voltage and current at rated magnitude and frequency will be considered as power quality issues [3]. Its impact is noticeable in minimum and high voltage power transmissions because it affects the life cycle of the equipment's. Distortion in the sinusoidal wave which is also known as harmonics is the cause which reduces the life of equipment's sensitive to the variation in power supply. In case of high voltage transmission such distortion tends to sag, swell, voltage dip etc in the transmission and ultimately harms the quality supply of power distribution. Increasing use of power electronic devices in home appliances as well as in industries in combination with non-linear loads and unbalanced loads are the main cause of injection of harmonics in the supply system [4]. Hence filtration of power becomes essential. Many power filters became introduce and shows their benefits in this application. Any power conversion approach which diminishes the TDI by getting the output voltage in steps and taking the output nearer to a sine wave are known as multilevel inversion [5].

Static synchronous compensator gives precise control, faster response, reactive compensation ability in transmission system devices. Multilevel inverter does the adjustments in amplitude and phase of the reactive component of current which is in AC side. This enables multilevel inverter to control the amount and direction of flow of the reactive power exchanged with the ac power system.

## II. BASICS

Cascaded H-Bridge (CHB) showed its popularity in the field of HVAC transmission and variable speed drive applications [6]. It involves H Bridge units as an inverter having series connection. This is true for every phase of the supply system. Separate sources are provided for every H-Bridge. The source act as a battery for a power system. There is series connection of inverter level having AC terminal voltage. Again four switches are provided S1 to S4 for this connection. Through different combinations of the four switches, S1-S4, each converter level can generate three different voltage outputs,  $+V_{dc}$ ,  $-V_{dc}$  and zero. A systematic output voltage which is building up can be generated by the series connection ac output voltage of the H Bridge inverter. This is nothing but summation of the individual converter output. Level of output voltage counts are different for different structures of MLI. With this topography, the number of output-phase voltage levels is defined by:

$$g = 2S + 1$$

Where, S= D.C. Sources counts,

g = Output Volts Level

For example in 7<sup>th</sup> Level,  $7 = 2 * 3 + 1$

$$g = 7$$

Distortions produced in the output waveform due to the presence of harmonic can be controlled by controlling angles of the levels.

Use of cascaded multilevel inverter can eliminate the requirement of:

- a) Heavy transformer for traditional inverters.
- b) Clamped diodes.
- c) Flying capacitors.

Along with the above, some other features of MLI are:

- 1) Conventional inverter are not that much suitable for high power and high voltage implementation. But in the case of MLI these implementations are possible.
  - 2) MLI gives pure sinusoidal waveform by incrementing level numbers [7].
  - 3) Each bridge is fed with a separate DC source, it does not require voltage balance (sharing) circuits or voltage matching of the switching devices as inverter structure itself consists of a cascade connection of many single-phase, full-bridge inverter (FBI) units
  - 4) To avoid bulky and loss resistor-capacitor-diode snobbery, soft-switching can be used.
- Due to the above features of MLI the harmonic analysis is possible.

## II. SIMULATION OF MULTILEVEL INVERTER:

Fig: 1 Simulation for 11 level inverter

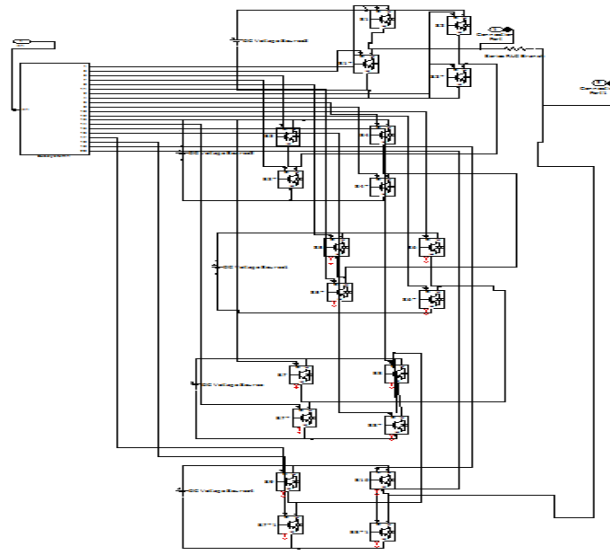
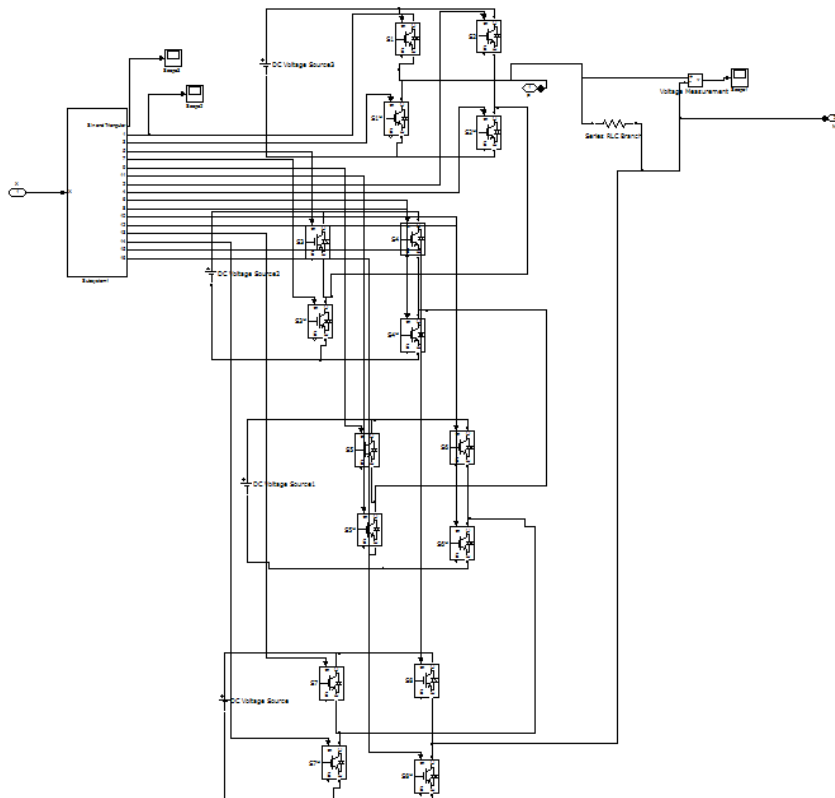
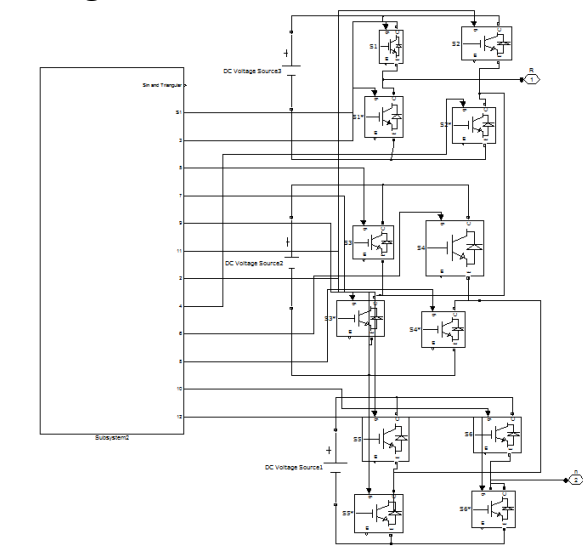


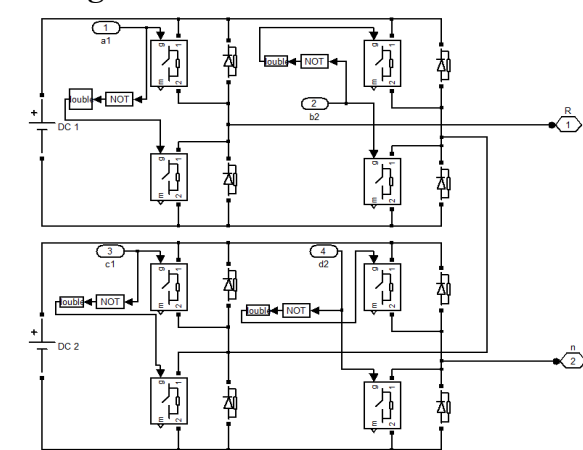
Fig: 2 Simulation for 09 level inverter



**Fig:3 Simulation for 07 level inverter**



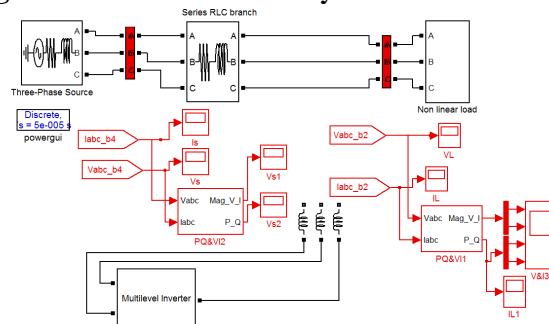
**Fig:4 Simulation for 05 level inverter**



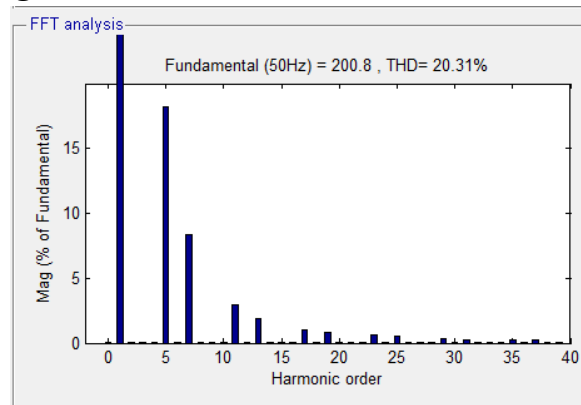
**Table1: Levels and percentage THD**

Sr. No.	Inverter levels	% THD
1	Five	35.03
2	Seven	14.30
3	Nine	13.68
4	Eleven	8.86

**Fig5: 11kV Transmission system without MLI**



**Fig:6 Without MLI source current THD result**

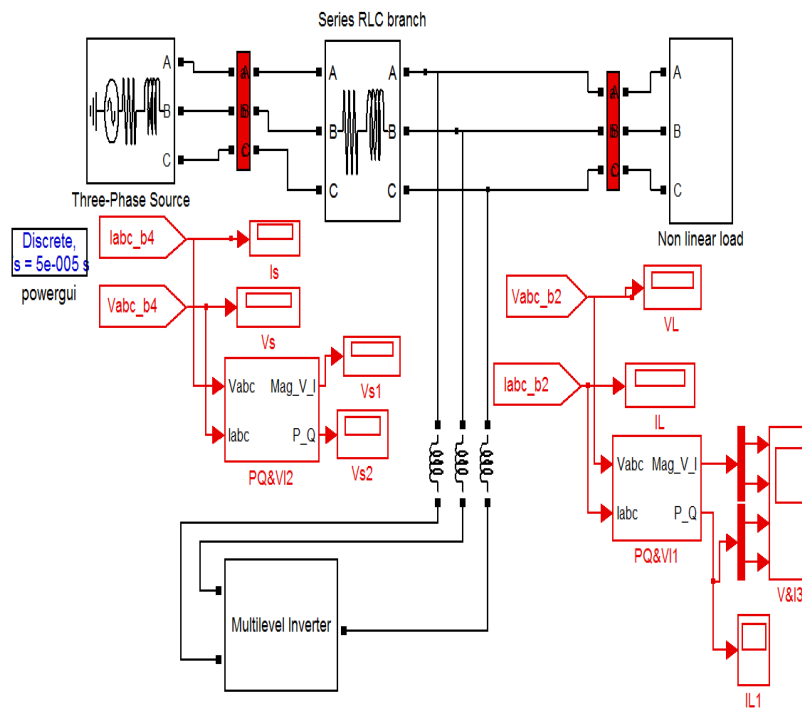


Various components with their ratings are shown in the following table.

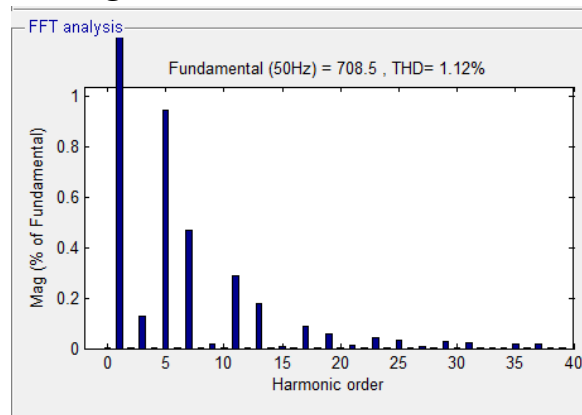
**Table2: Ratings of components**

Sr. No	Component	Rating
1	Supply System	11kV,50Hz
2	Source resistance	0.1 ohm
3	Source inductance	0.9 mH
4	3φRLC branch	R-10 Ω, L-10mH
5	Universal bridge as nonlinear load	Snubber R-1*10 <sup>6</sup> , Snubber C-1*10 <sup>-12</sup>
6	Inductance	10*10 <sup>-3</sup>
7	Subsystem 1	Rs-0.4Ω, L-1642*10 <sup>-6</sup> H C- 10*10 <sup>-6</sup> F, Rp- 20Ω
8	Subsystem IGBT(16)	2 Internal resistance-1*10 <sup>-3</sup> Ω Snubber resistance-1*10 <sup>5</sup> Ω Snubber

**Fig:7 11kV System with MLI**



**Fig8: Source current THD result**



## CONCLUSION

In this paper comparison between different MIL has been done. Also an 11kV transmission system has been simulated for THD in current with MLI and without MLI. It has been found that the percentage reduction in THD is fairly better. From the simulation result it has been proved that the three phase 11 levels MLI with suitable modulation technique, when used for 11 kV transmission line has alleviated voltage sag, unbalancing and provided harmonic reduction under various load fluctuating conditions.

## REFERENCES

- [1] FengLiu, ShengweiMeia, QiangLuo, Yixin Nib, Felix F. Wub, Akibiko Yokoyama "The nonlinear internal control of STATCOM: theory and application" *Electrical Power and Energy Systems*, 25 (2003) 421–430.
- [2] Shoorangiz S.S., Farabani, Reza Hemati and Mehdi Nikzad "Comparison of Artificial Intelligence Strategies for STATCOM Supplementary Controller Design" *World Applied Sciences Journal* 7 (11): 1428-1438, 2009.
- [3] Design and Simulation of Cascaded H-Bridge Multilevel Inverter Based DSTATCOM J. Ganesh Prasad Reddy1, K. Ramesh Reddy2. *International Journal of Engineering Trends and Technology- Volume 3 Issue 1- 2012*
- [4] Harmonic analysis of seven and nine level cascade multilevel inverter using multi-carrier pwm technique 1chetanya gupta, 2 devbratkuar, 3abbisbek varshney, 4tabir khurshaid  
1,2,3,4School of Electrical, Electronics and Communication Engineering Dept., Galgotias University, Greater Noida, India
- [5] IEEE TRANSACTIONS ON POWER ELECTRONICS, VOL. 19, NO. 2, MARCH 2004 *A Unified Approach to Solving the Harmonic Elimination Equations in Multilevel Converters* John N. Chiasson, Senior Member, IEEE, Leon M. Tolbert, Senior Member, IEEE
- [6] *Multilevel Inverters: A Survey of Topologies, Controls, and Applications*  
José Rodríguez, Senior Member, IEEE, Jib-Sheng Lai, Senior Member, IEEE, and Fang Zheng Peng, Senior Member, IEEE  
IEEE TRANSACTIONS ON INDUSTRIAL ELECTRONICS, VOL. 49, NO. 4, AUGUST 2002
- [7] Reduction of Total Harmonic Distortion in Nine level Cascaded Multilevel Inverter with different types of load  
Chetanya Gupta1, Gajendra Sharma2, Abbisbek Varshney3, Rajneesh Subalka4, *IOSR Journal of Electrical and Electronics Engineering (IOSR-JEEE)* e-ISSN: 2278-1676, p-ISSN: 2320-3331, Volume 9, Issue 6 Ver. III (Nov – Dec. 2014), PP 12-20 [www.iosrjournals.org](http://www.iosrjournals.org)